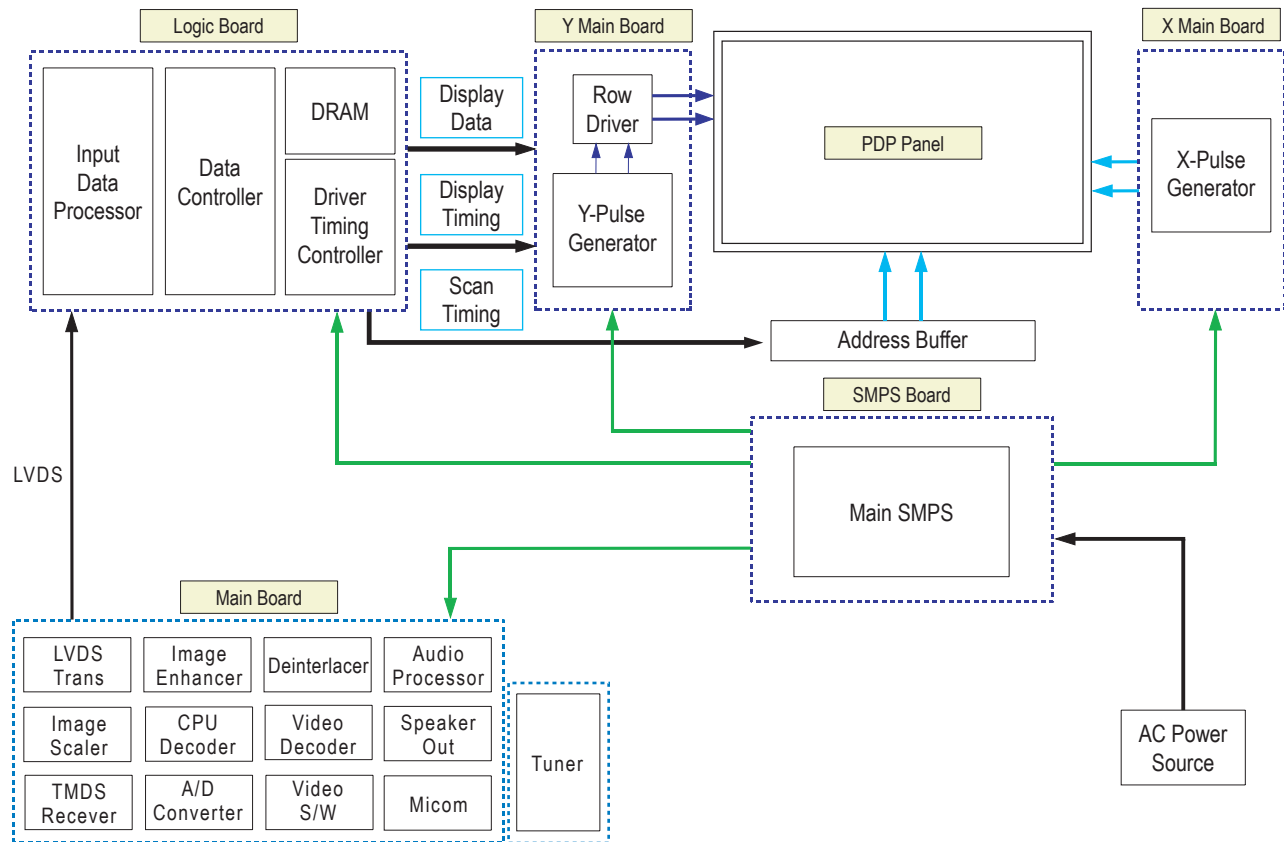


7. Schematic Diagram

7-1 Circuit Description



■ SMPS Board

The SMPS used for the PDP has been designed to be efficient, compact and lightweight. For VS and VA outputs, a LLC converter has been used. For the other outputs, a Flyback converter has been used.

■ LOGIC Board

The logic circuit consists of a Logic Main Board and an Address Buffer Board. The Logic Main Board decodes the video signal encoded by the Video Board, outputs the ADDRESS data signal for each pattern and generates X and Y drive signals. The Address Buffer Board buffers and transfers the ADDRESS data output signal using TCP IC.

- LVDS with built-in video signal processing (W/L, error diffusion, APC, FCR, etc.) applied and 1 ASIC chip.
- Outputs the address Drive IC control and data signals to the Buffer Board.
- Outputs the control signal for the X and Y Drive Boards.
- Monitors major drive voltages (Micom Circuit Block); detects if a surge voltage has been applied and protects the Drive Circuit.
- Temperature Adaptive Operating Mode (Low Temperature/Room Temperature/High Temperature); Discharge optimization for each temperature level.

■ X-MAIN Board

Connects to the X terminal block, 1) provides maintaining voltage waveform (including ERC), and 2) maintains the Ve bias in the Scan section.

■ Y-MAIN Board

Connects to the Y terminal block, 1) provides maintaining voltage waveform (including ERC), 2) provides Y Rising, Falling Ramp waveforms, and 3) maintains the Vscan bias.

■ Address Buffer Board

It delivers the data signal and control signal to the TCP.

MEMO

7-2 Schematic Diagram

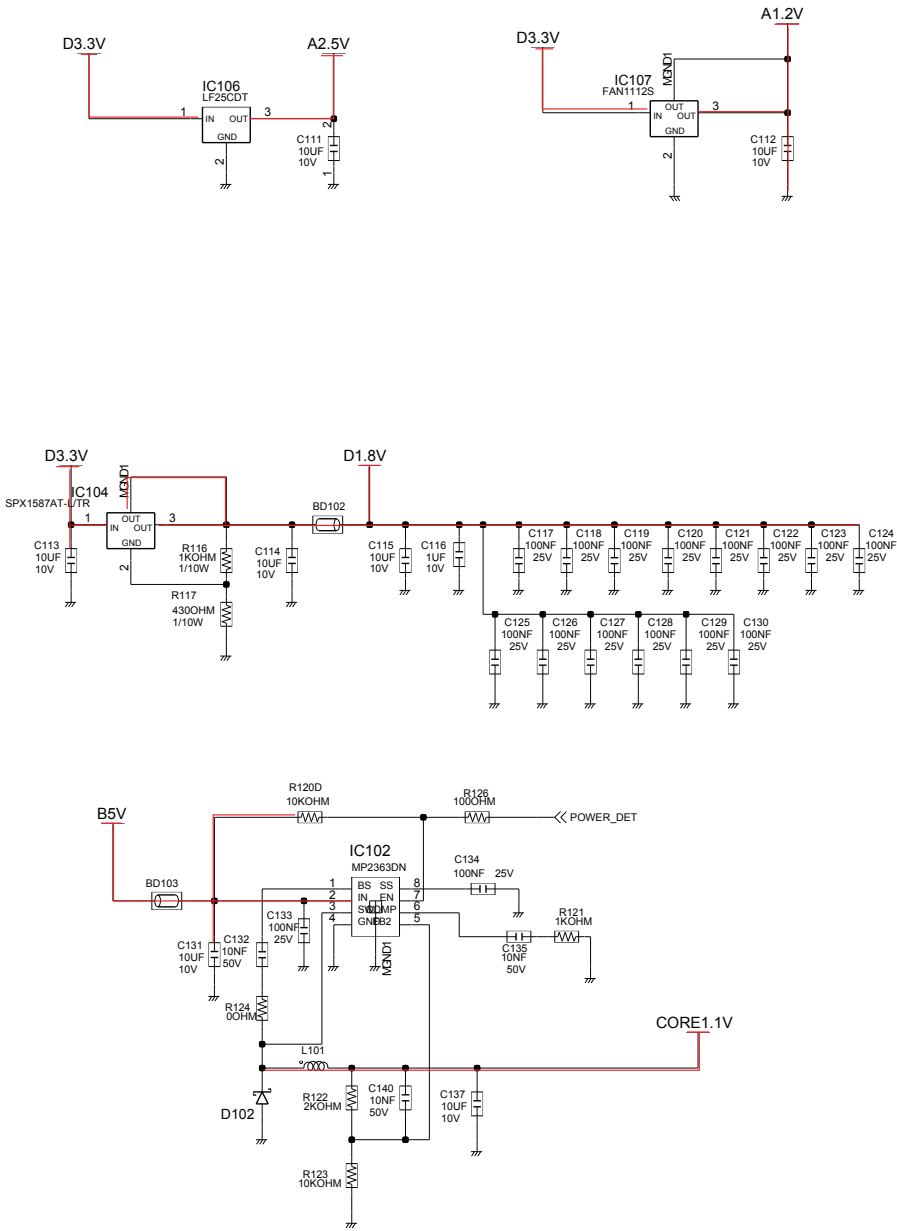
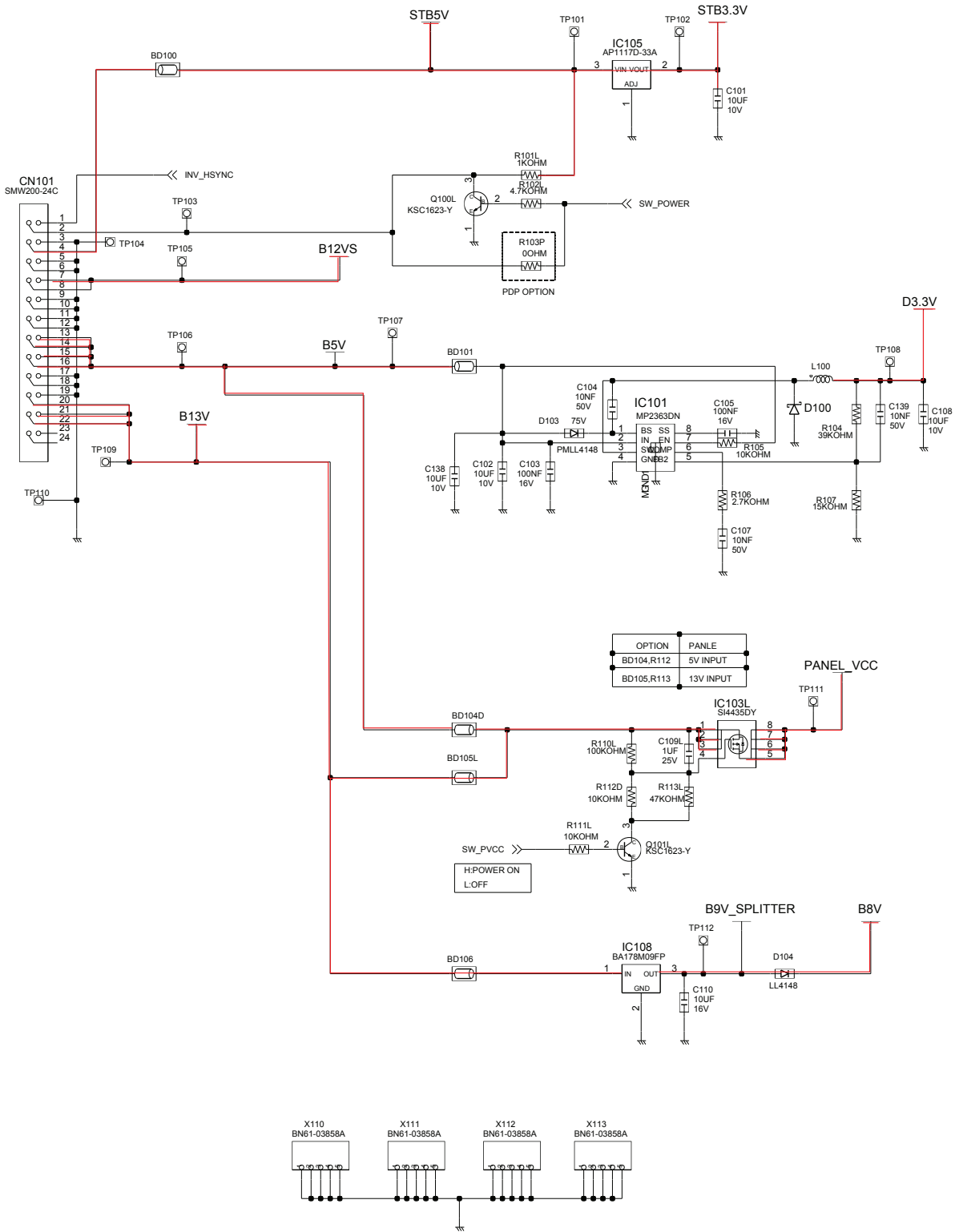
7-2-1 Power

Power

STANDBY POWER

LOCATION SUFFIX

PDP	P	OPTION	C
LCD	L	SIDEAV	S
DELETE	D	ON BOARD	B
HOTEL	H	FUNCTION I2S	I

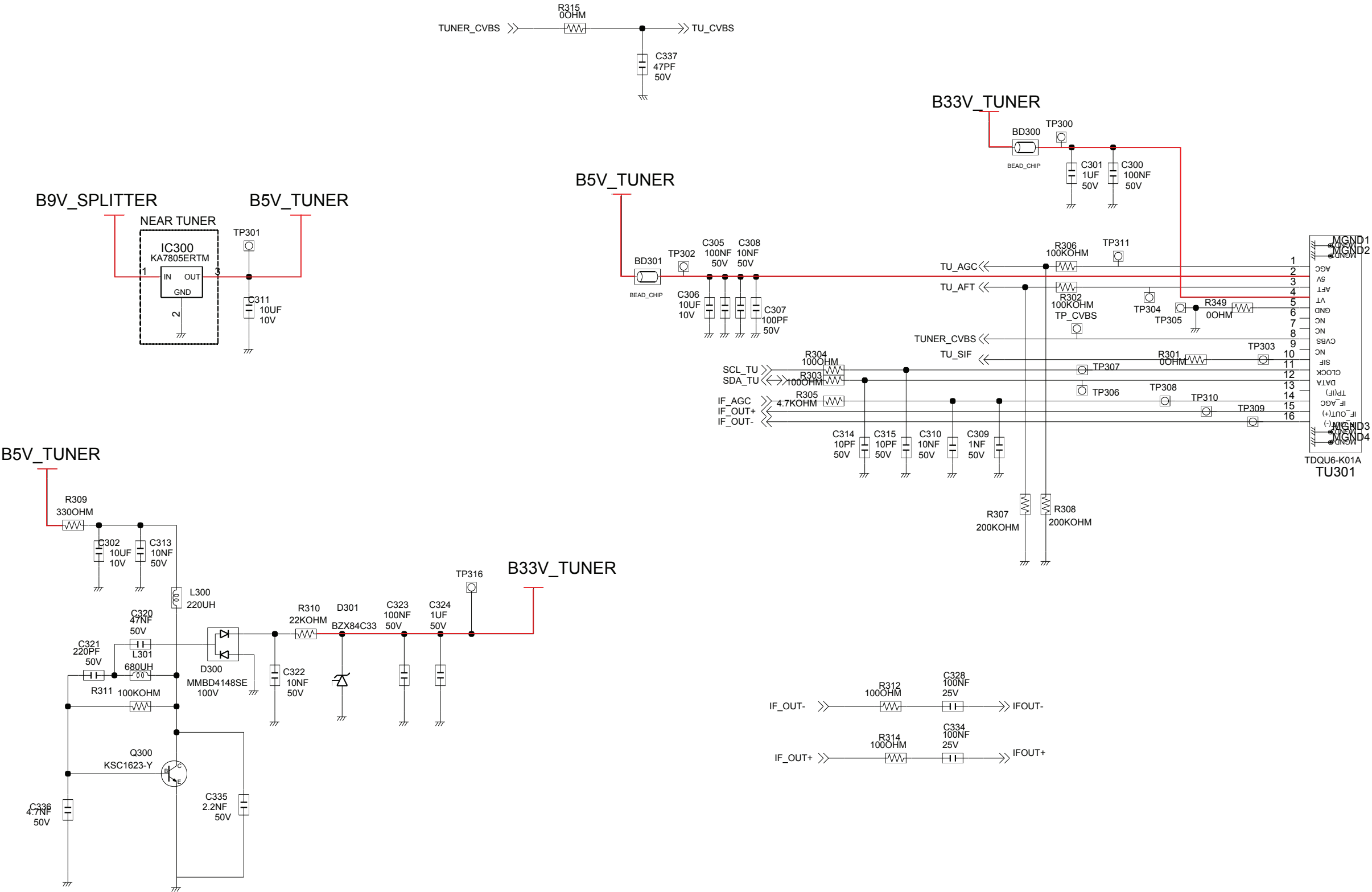


7-2-3 HDMI PDP_noise

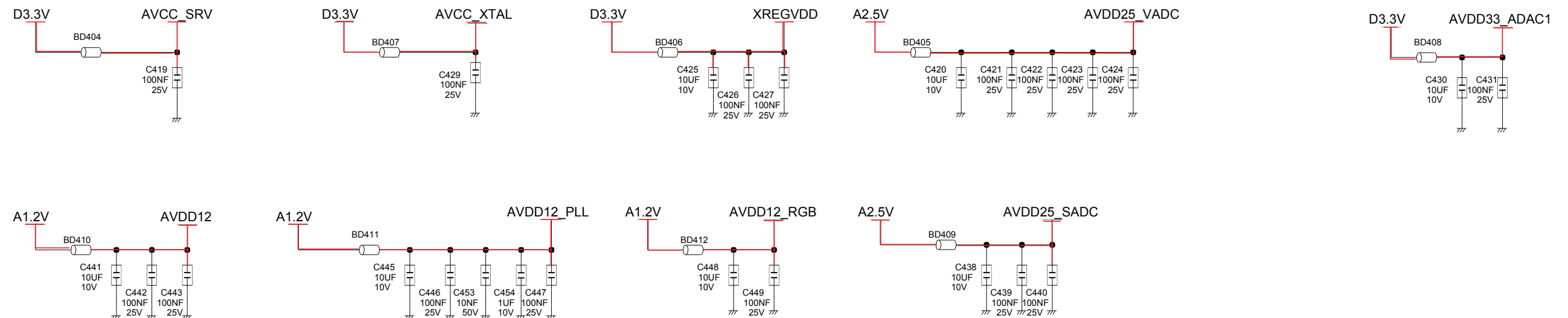
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ATSC HALF NIM TUNER

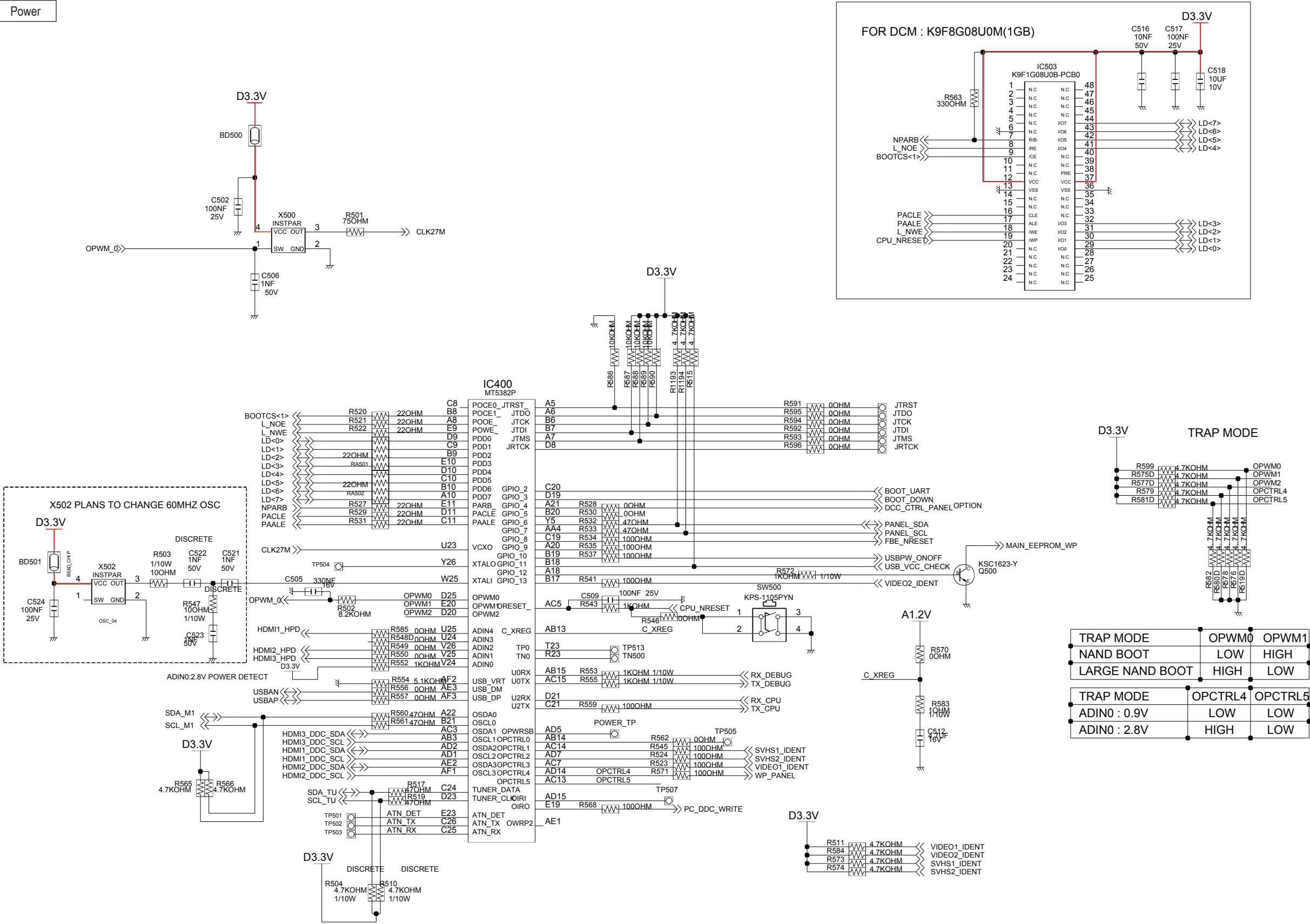


POWER BLOCK DON'T CLOSED TO IC(TUNER)

[illegible]

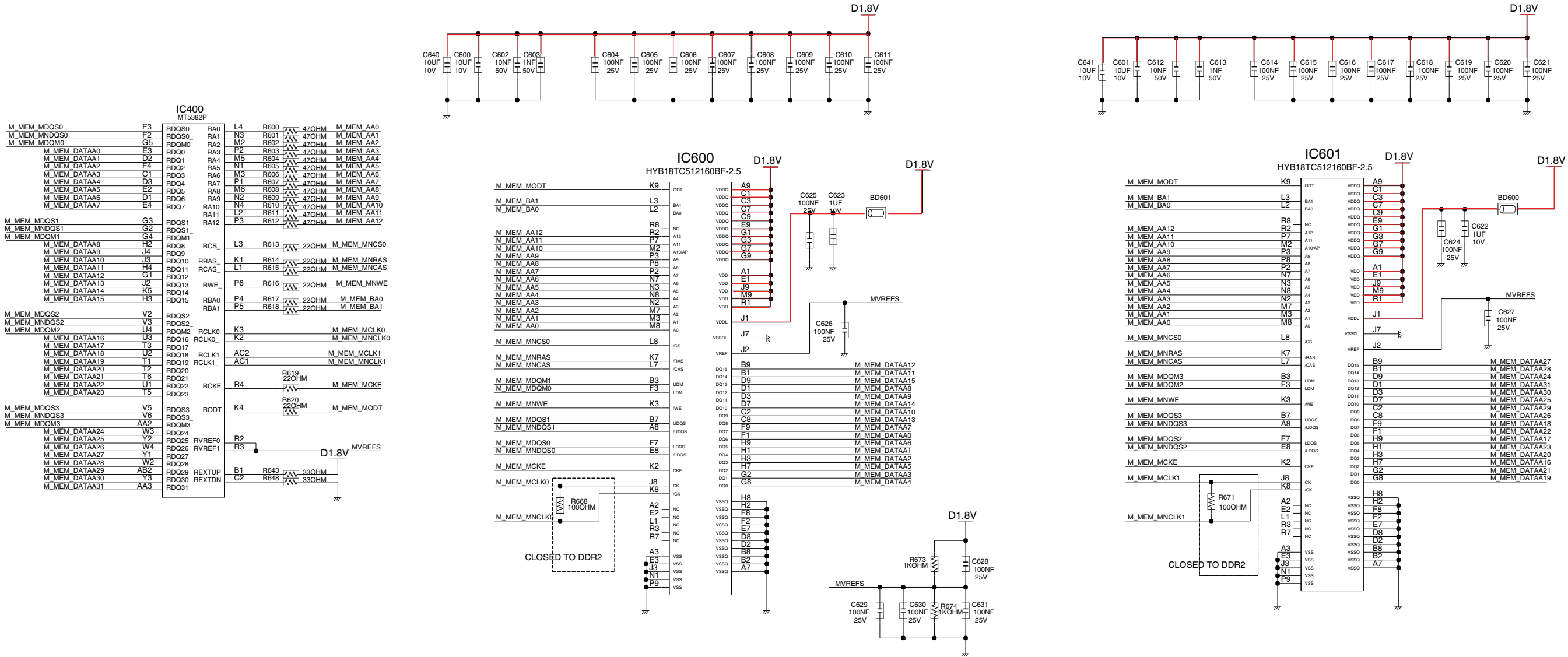
7-2-5 Input & Output Jack I

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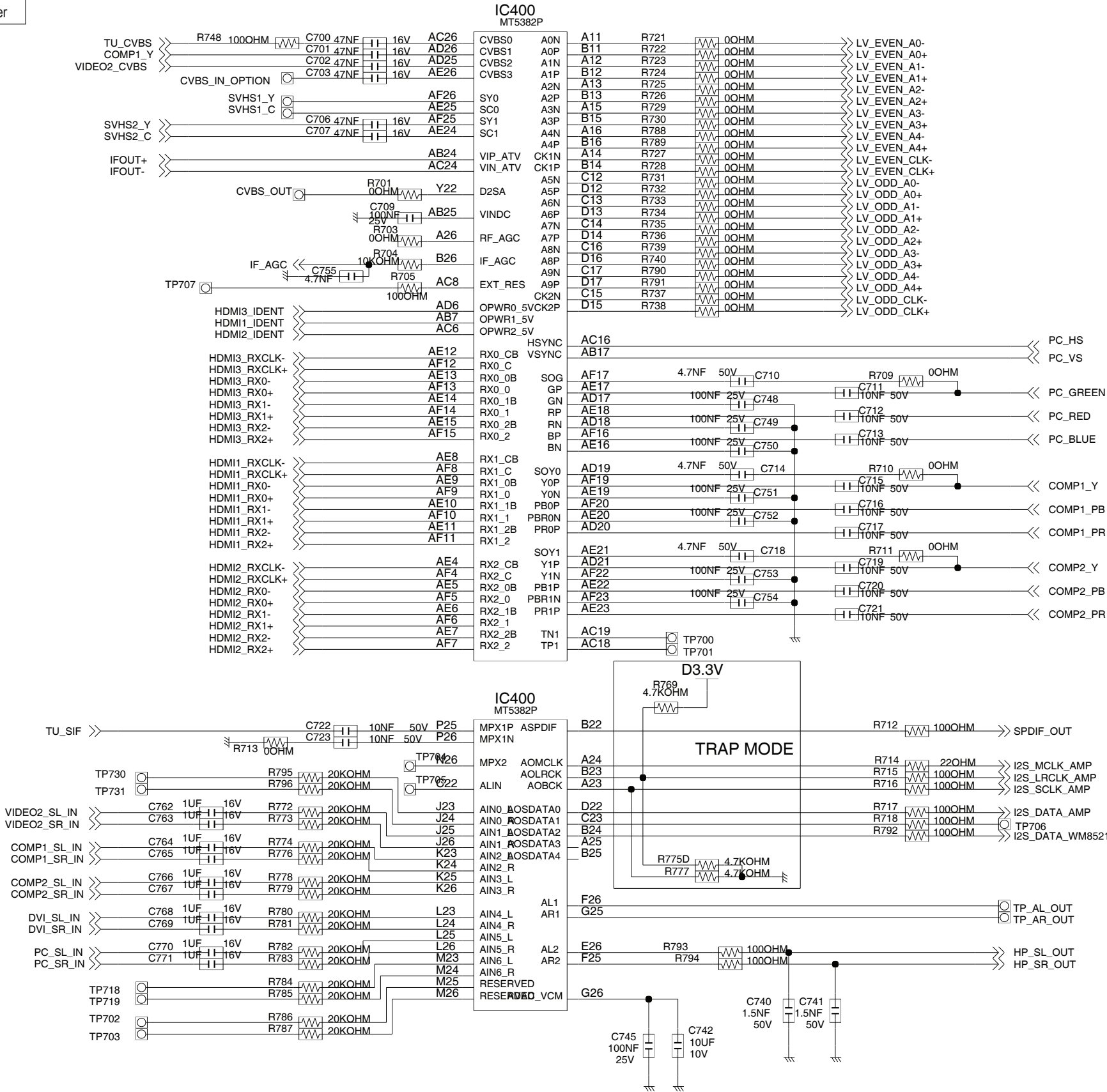
7-2-6 Input & Output Jack II

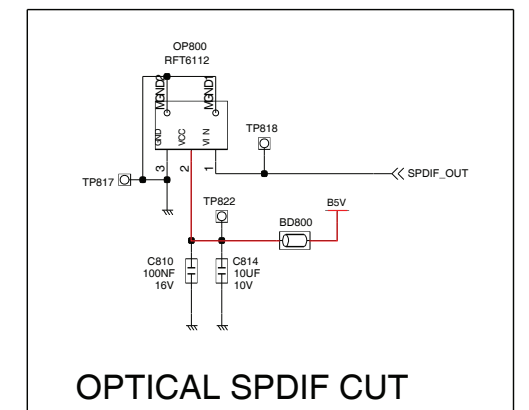
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7-2-7 Sub micom

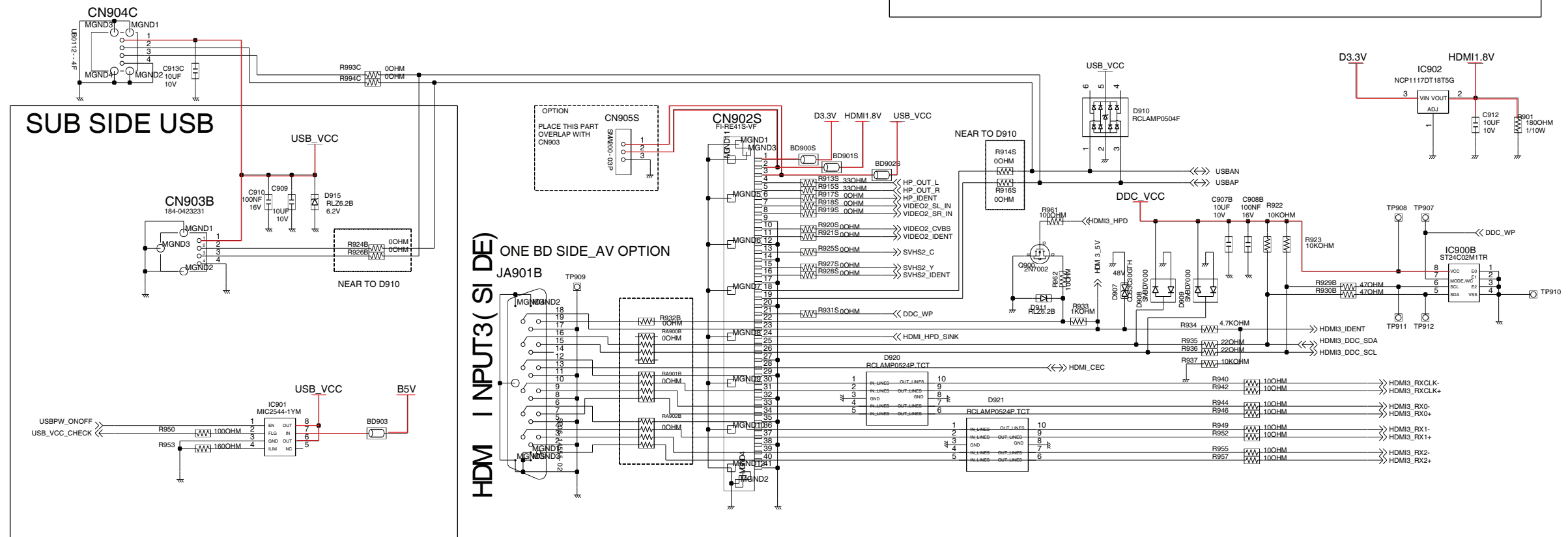
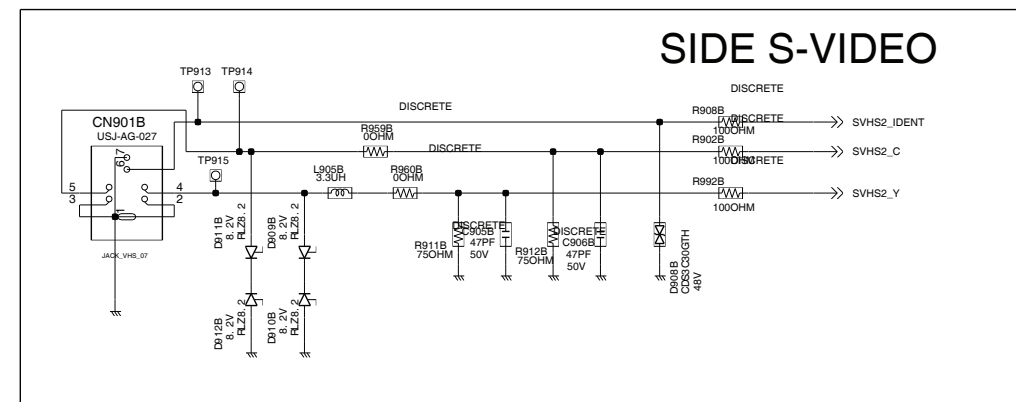
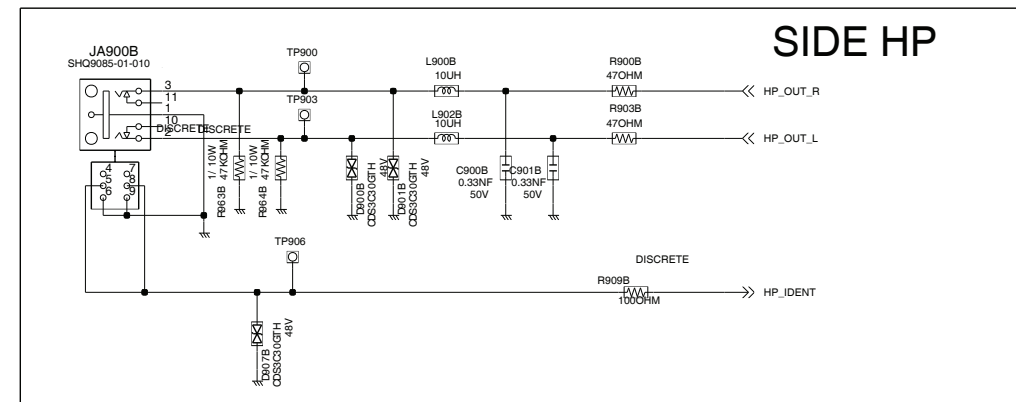
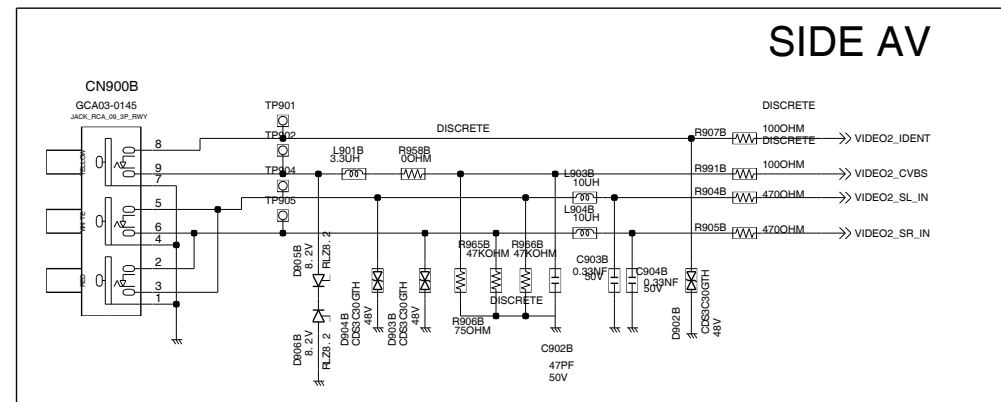
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7-2-9 ADC & SVP-WX Power

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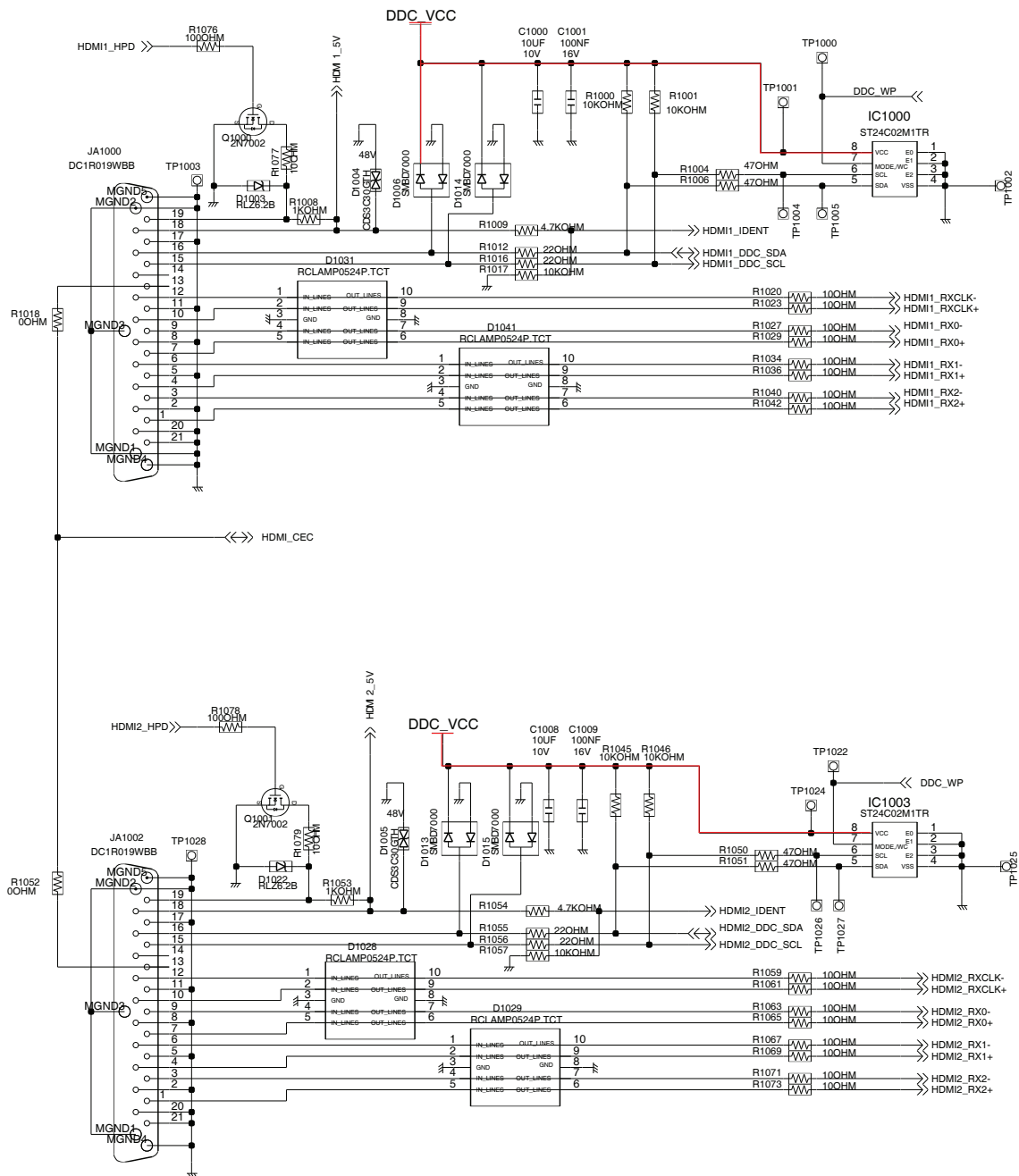
SIDE HDMI/USB CONNECTOR

7-2-10 SCALER

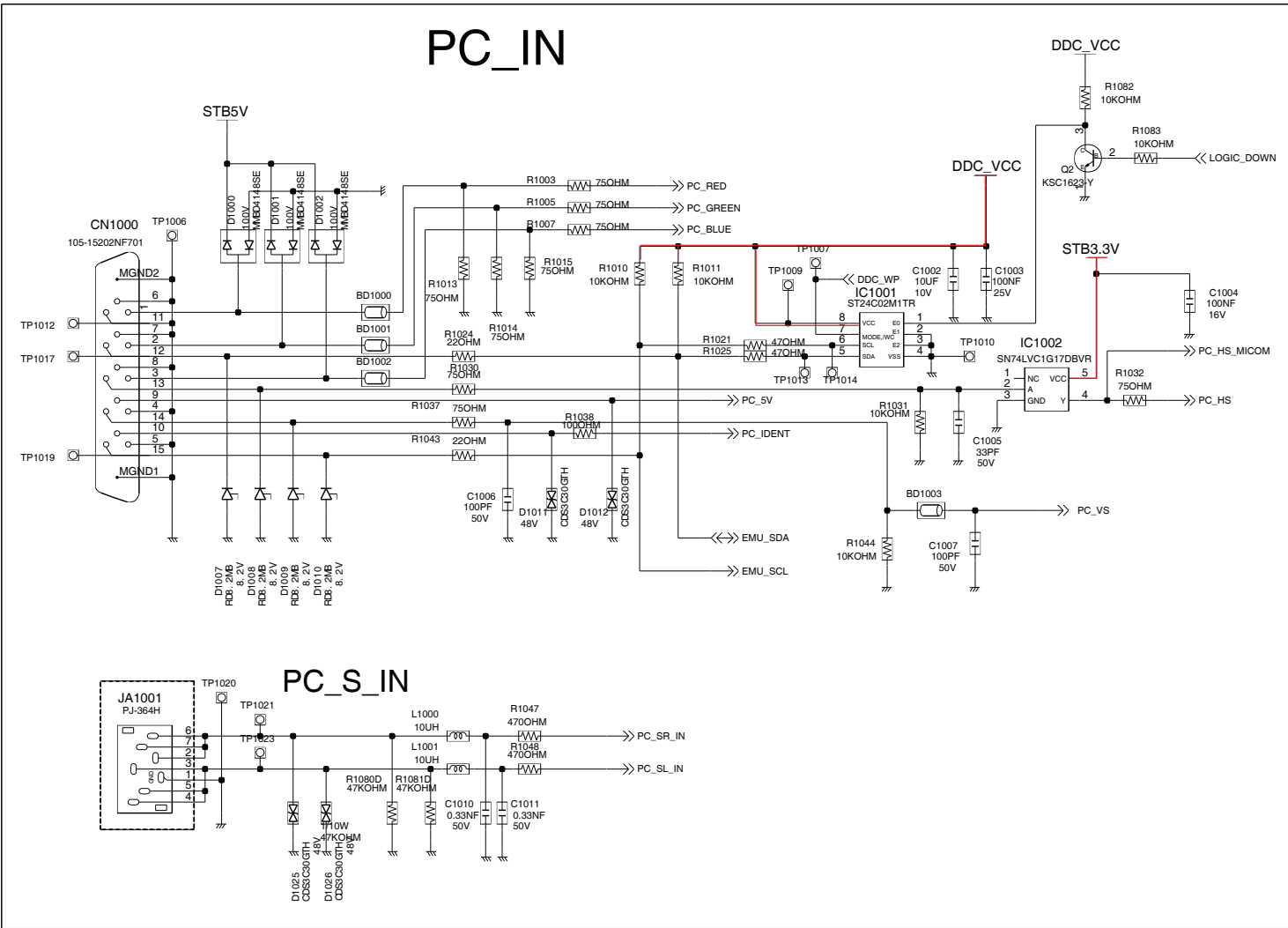
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HDMI INPUT1



HDMI INPUT2



7-2-11 DDR_TUNER, LVDS Out

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